This listing of claims will replace all prior versions and listings of claims in the application:

## **Listing of Claims:**

Claim 1 (Currently amended) A digital tuner having an input tuning range with a lower frequency limit and an upper frequency limit, said tuner comprising:

an upconverter for converting an input signal to an intermediate frequency signal whose frequency is higher than said upper frequency limit of said input tuning range, said upconverter having a local oscillator fundamental frequency which is greater than said upper frequency limit of said input tuning range; and

a zero intermediate frequency quandrature downconverter for converting said intermediate frequency signal from said upconverter to in-phase and quadrature baseband signals-;

wherein said upconverter is a substantially fixed upconverter and said downconverter is a variable downconverter for selecting a desired channel.

## Claim 2-3 (Canceled)

Claim 4 (Original) A tuner as claimed in claim 1, in which said upconverter is arranged to convert said input tuning range to an intermediate frequency range having a lower frequency limit and an upper frequency limit less than twice said lower frequency limit of said intermediate frequency range.

Claim 5 (Original) A tuner as claimed in claim 1, in which said tuner has an input, said downcoverter has an input, and there is no frequency filtering between said input of said tuner and said input of said downconverter.

Claim 6 (Original) A tuner as claimed in claim 1, comprising first and second baseband filters for filtering said in-phase and quadrature baseband signals from said downconverter.

Claim 7 (Original) A tuner as claimed in claim 6, in which said first and second filters are low pass filters.

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Claim 8 (Original) A tuner as claimed in claim 7, in which each of said low pass filters has a variable cut-off frequency, said tuner comprising a controller for controlling said variable cut-off frequency in accordance with a bandwidth of a received channel.

Claim 9 (Original) A tuner as claimed in claim 8, comprising a symbol rate detector for indicating said bandwidth of said received channel to said controller.